



09-14-04

1FW9

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors: Angelo Magri', Ferruccio Frisina

Title: Insulated Gate Planar Integrated Power Device With Co-Integrated Schottky Diode and Process

Serial No.: 10/713,778

Filing Date: November 14, 2003


Atty Dkt. No.: 2110-86-3

CERTIFICATE OF MAILING OR TRANSMISSION

"Express Mail" mailing label number: EV803535165US

Date of Deposit: September 12, 2006

I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR, Section 1.10 on the date indicated above and is addressed to Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 by

  
Signature

REQUEST FOR EXTENSION OF TIME

September 12, 2006

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

TO THE COMMISSIONER FOR PATENTS:

Applicant respectfully requests that the shortened statutory period for response to the outstanding Office Action mailed April 12, 2006, now set to expire on July 12, 2006, be extended two (2) months to expire on September 12, 2006.

Enclosed is our check No. 26508 in the amount of \$450.00 for a two-month Extension of Time.

Please charge any additional fees or credit overpayment to Deposit Account No. 07-1897.

Respectfully submitted,

GRAYBEAL JACKSON/HALEY LLP



---

Paul F. Rusyn  
Attorney for Applicant  
Registration No. 42,118  
155-108th Avenue NE, Suite. 350  
Bellevue, WA 98004-5973  
(425) 455-5575